

# Systematic Error Analysis in a Josephson Impedance Bridge

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Josephson impedance bridges are under development by four European metrology institutes: SP (Sweden), VTT (Finland), CEM (Spain) and PTB. All four bridges are based on programmable Josephson arrays, but each bridge has slight variations in the setup, number of samples, degree of automation and adjustment procedures. The Josephson impedance bridge from PTB uses two individual Josephson systems, connected directly to the impedance standards, as shown below. Each system consists of a Josephson array driven at 70 GHz and biased with NPL designed bias sources. The bridge voltage is measured by a Lock-In Amplifier (LIA) in combination with a pre-amplifier (PA). The number of samples is limited to two, resulting in the smallest error of ac voltage. A computer runs a frequency sweep from 25 Hz up to 10 kHz in typically 40 min. Reversing of the impedance standards is still manual, since no suitable change over switch has been implemented. The bridge is automatically balanced in amplitude by changing the rf frequency and in phase by a dual channel function generator controlling the timing of the bias sources.

